

**ANNAMACHARYA INSTITUTE OF TECHNOLOGY AND SCIENCES  
KADAPA**

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**ANNAMACHARYA INSTITUTE OF TECHNOLOGY AND SCIENCES  
KADAPA**

**Utukuru (P), C.K. Dinne (V & M)**

2017-2021 Batch

**CO & PO attainment through Direct Assessment**

Course code and name	%CO attainment					% PO attainment-												
	CO1	CO2	CO3	CO4	CO5	PO1	PO2	PO3	PO4	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
15A04511 Computer Organization	87	85	85	85	87	90	90	87	85	87	85			90	90	87	87	90
15A04501 Antennas and Wave Propagation	87	87	87	85	85	85	70	85	80	85	85				75			85
15A04502 Digital Communication Systems	87	85	87	87	87	85	87	85	85	85	80	85	85			85		85
15A04503 Linear Integrated Circuits and Applications	87	85	87	85	86	85	5	87	87	85	87							85
15A04504 Digital System Design	85	87	86	85	86	85	85	85	87	85	85	87						87
15A04507 IC Applications Laboratory	85	87	85	85	86	85	87	87	85	87	85	87				87		85
15A04508 Digital Communication Systems Laboratory	87	85	86	87	85	87	85	86	87	87	87	87						85
15A99501 Audit course – Social Values & Ethics	87	85	84	86	87		87	87	85		87	87	87					85
15A52301 Managerial Economics and Financial Analysis	87	85	84	85	85		87	87	87	85	85							86
15A04601 Microprocessors & Microcontrollers	87	87	87	85	85	85	87	87	85	85								87
15A04602 Electronic Measurements and Instrumentation	87	85	87	87	87	85	85	85	85	87	85	85						87
15A04603 Digital Signal Processing	85	85	87	85	87	85	85	84	85	85	87	85						84
15A04604 VLSI Design	87	85	85	89	85	87	87	87	87	85	87	87						87
15A04607 Microprocessors & Microcontrollers Laboratory	85	87	85	87	87	87	85	87	87	87	87	87						85

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C.K. Dinne (A.P.)

15A04608 Digital Signal Processing Laboratory	87	87	85	86	87	87	85	87	85	87	85	87	85	87	85	87	85	85			
15A52602 Advanced English Language Communication (ABELCS) Laboratory (Audit Course)	87	87	87	87	85	85	85		85				85		85	87	85	85	85		
15A04609 Comprehensive Online Examination-II	87	85	85	87	85		85		85	85	85							85	85		
15A04701 Optical Fiber Communication	87	87	85	87	87	87	85	87	85	85	85	85	85	85	85	85	85		87	85	
15A04702 Embedded Systems	87	85	87	87	87	85	85	85	85	87	85	87	87						87	85	
15A04703 Microwave Engineering	87	85	87	87	87	87	85	87	85	85	87	86	87						87	85	
15A04704 Data Communications and Networking	87	85	85	85	87	84	87	87	85	85	87	87	87						87	87	
15A04705 Radar Systems	85	87	87	87	87	85	87	85	87	85	87	87	85						87	85	
8515A04711 Microwave and Optical Communication Laboratory	87	85	85	87	85	87	85	87	85	85	87	86							87	87	
15A0471 VLSI & Embedded Systems Laboratory	87	85	86	87	87	85	87	85	87	87	87	87							87	87	
15A04802 Low Power VLSI Circuits & Systems	87	85	86	85	85	87	85	85	87	85	87	87							87	87	
15A04804 RF Integrated Circuits	87	85	87	87	87	87	85	86	87	87	87	87							85	85	
15A04805 Comprehensive Viva Voce	87	87	87	87	87	87	87	87	87										87	87	
15A04806 Technical Seminar	87	87	87	87	87	87	87	87	87	87	87	87							87	87	
15A04807 Project Work	87	87	87	87	85	87	87	87	87	87	87	85							87	85	
% CO & PO attainment	86	85	86	86	86	86	86	86	82	86	85	86	85	86	85	85	86	86	84	86	86

*Asst. Prof.*

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Uthukur (P), C.K. Dinnu (V & M), Kadapa (Dh)



### Co & Po attainment through Indirect Assessment

	% CO Attainment					% PO Attainment												
	CO1	CO2	CO3	CO4	CO5	PO1	PO2	PO3	PO4	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
survey	85	86	87	85	85	85	86	87	85	85	87	85	87	89	86	87	87	86
Employee survey	85	86	87	85	85	85	86	87	85	85	87	85	87	87	86	87	87	86
Alumni survey	85	87	87	85	85	85	86	86	87	87	85	87	87	89	85	87	85	85
Graduate survey	85	87	87	87	85	87	87	87	85	85	87	85	85	89	89	87	85	87
Indirect survey	87	85	85	88	85	89	85	86	86	87	85	85	85	81	81	87	87	84
% CO & PO attainment	85	82	83	86	85	86	85	86	85	86	85	86	85	86	85	87	86	85

  
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**Overall Attainment of CO & PO**

	CO1	CO2	CO3	CO4	CO5	PO1	PO2	PO3	PO4	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12
% CO & PO attainment(direct)	86	85	86	86	86	86	82	86	85	86	85	85	86	86	84	86	86	86
% CO & PO attainment(indirect)	85	82	83	86	85	86	85	86	85	87	86	86	85	86	85	87	86	85
Overall attainment	85.5	83.5	84.5	86	85.5	86	83.5	86	85	86.5	85.5	85.5	85.5	86	84.5	86.5	86	85.5
Attainment scale	5	5	5	5	5	5	5	5	5	5	5	5	5	5	5	5	5	5

*Accepted*

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*Set*

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